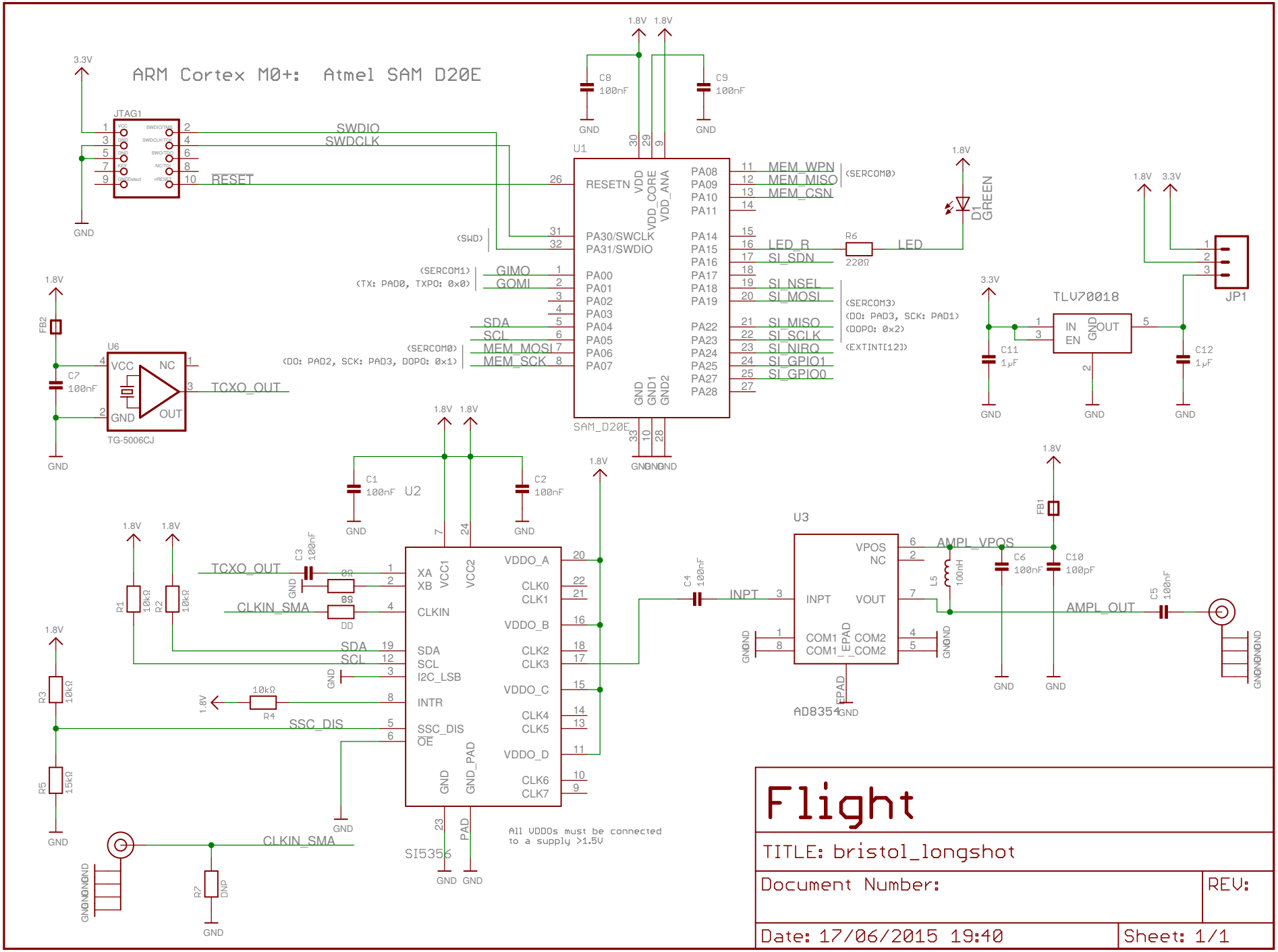
ARM Cortex M0+: Atmel SAM D20E
JTAG1
SWDIO
SWDCLK
RESET
U1
SAM_D20E
RESETN
PA30/SWCLK
PA31/SWDIO
PA00
PA01
PA02
PA03
PA04
PA05
PA06
PA07
PA08
PA09
PA10
PA11
PA14
PA15
PA16
PA17
PA18
PA19
PA22
PA23
PA24
PA25
PA27
PA28
MEM_WPN
MEM_MISO
MEM_CSN
LED_R
SI_SDN
SI_NSEL
SI_MOSI
SI_MISO
SI_SCLK
SI_NIRQ
SI_GPIO1
SI_GPIO0
GIMO
GOMI
SDA
SCL
MEM_MOSI
MEM_SCK
(SERCOM0)
(SWD)
(SERCOM1)
(TX: PAD0, TXPO: 0x0)
(SERCOM0)
(DO: PAD2, SCK: PAD3, DOPO: 0x1)
(SERCOM3)
(DO: PAD3, SCK: PAD1)
(DOPO: 0x2)
(EXTINT[12])
R6
220Ω
LED
D1
GREEN
JP1
TLV70018
IN
EN
OUT
GND
C11
1µF
C12
1µF
U6
TG-5006CJ
VCC
NC
GND
OUT
TCXO_OUT
FB2
C7
100nF
C8
100nF
C9
100nF
C1
100nF
C2
100nF
U2
SI5356
XA
XB
VCC1
VCC2
CLKIN
SDA
SCL
I2C_LSB
INTR
SSC_DIS
OE
GND_PAD
VDDO_A
CLK0
CLK1
VDDO_B
CLK2
CLK3
VDDO_C
CLK4
CLK5
VDDO_D
CLK6
CLK7
PAD
CLKIN_SMA
R1
10kΩ
R2
10kΩ
R3
10kΩ
R4
10kΩ
R5
15kΩ
R7
DNP
C3
100nF
SSC_DIS
All VDDOs must be connected to a supply >1.5V
U3
AD8354
VPOS
NC
INPT
VOUT
COM1
COM2
EPAD
AMPL_VPOS
AMPL_OUT
L5
100nH
C6
100nF
C10
100pF
C4
100nF
C5
100nF
FB1
Flight
TITLE: bristol_longshot
Document Number:
REV:
Date: 17/06/2015 19:40
Sheet: 1/1