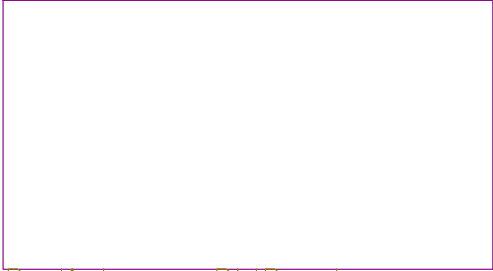


Power



Radioberry-PWR.sch

GPIO

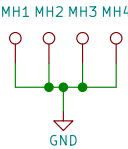


Raspberry-GPIO.sch

Frontend



Radioberry-Frontend.sch



PA3GSB  
AppMind

Sheet: /  
File: Radioberry.sch

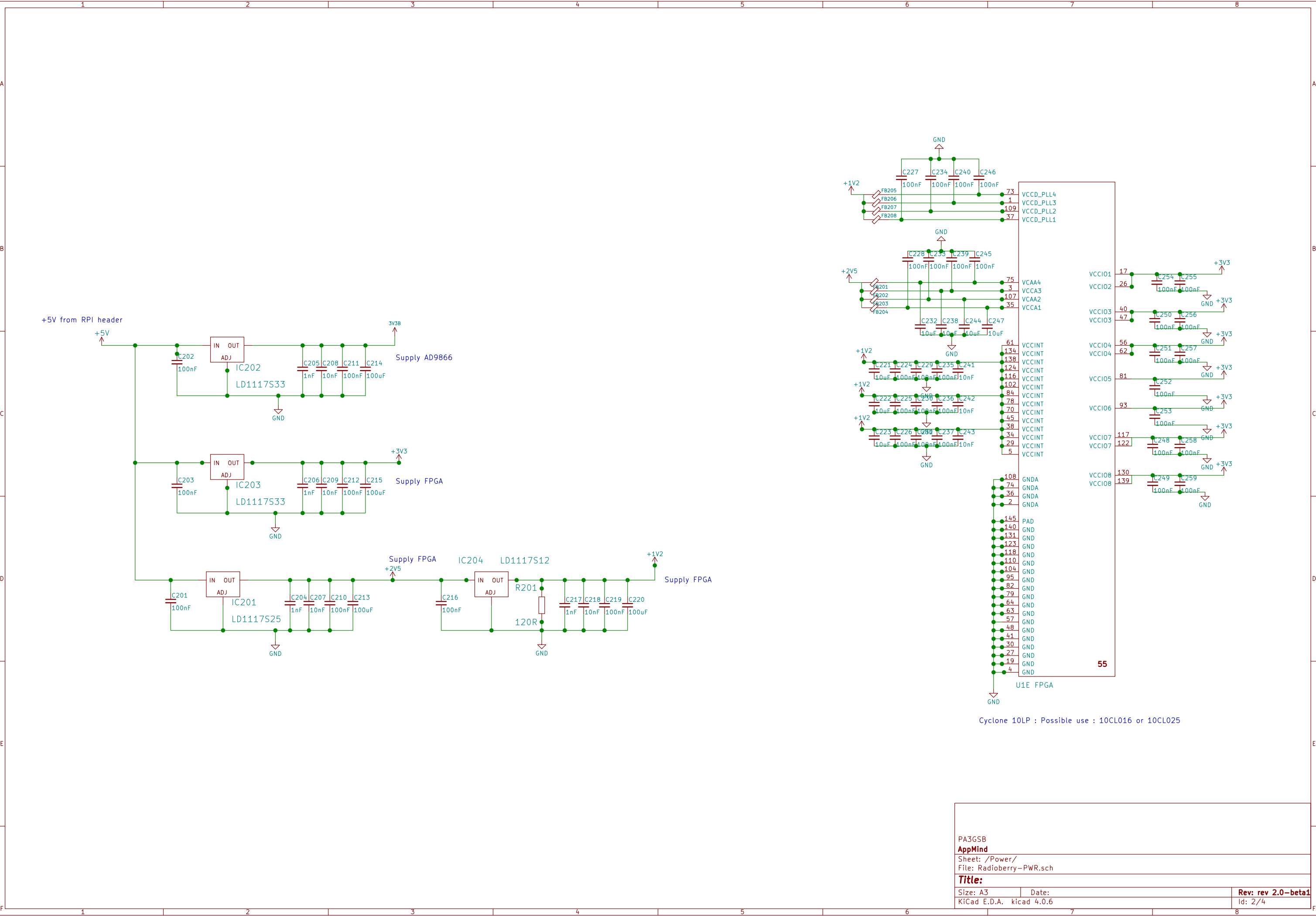
**Title: RadioBerry V2.0**

Size: A4      Date: 2017-11-29

KiCad E.D.A.    kicad 4.0.6

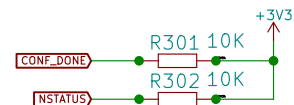
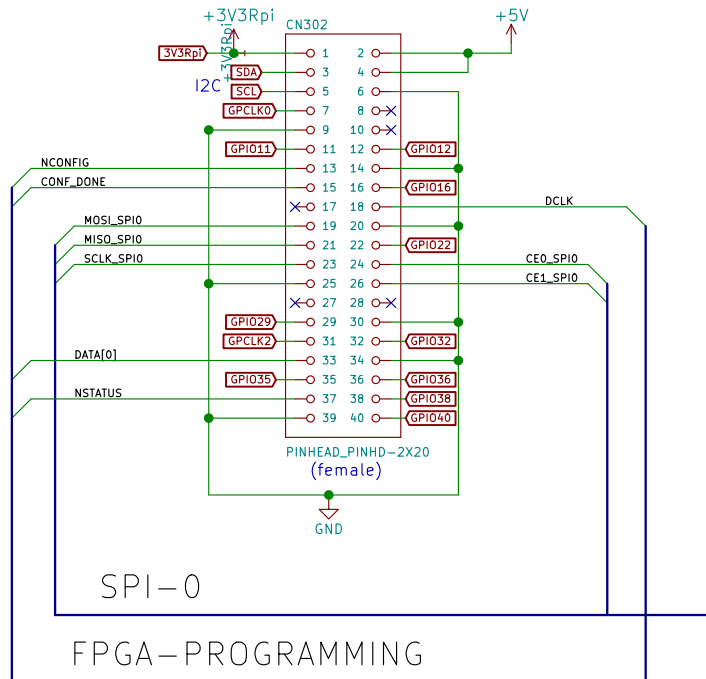
**Rev: beta2**

Id: 1/4

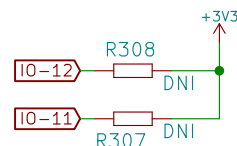


## Attach to RPI GPIO Header connector

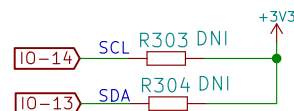
PWR\_FLAG  
+3V3Rpi



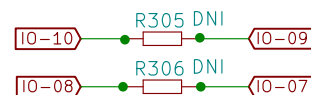
Programming pull-ups



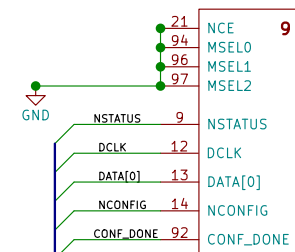
Option pull-ups  
(internal FPGA pull up to large / avoiding speed problems)



Option I2C by FPGA

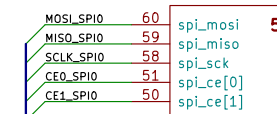


Option LVDS - Termination



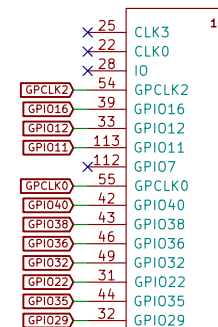
U1A FPGA

FPGA-PROGRAMMING

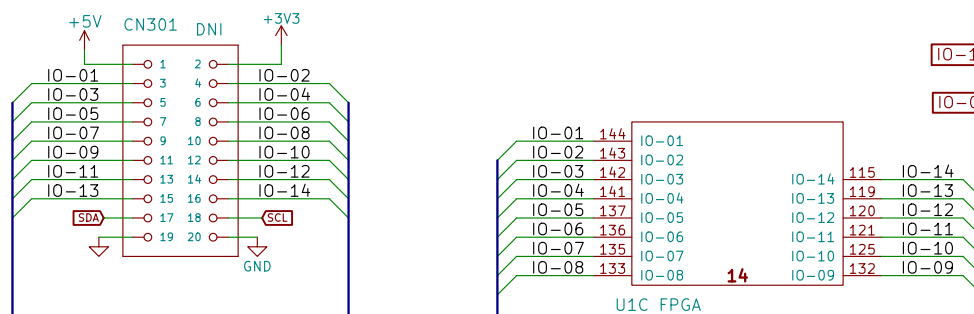


U1B FPGA

SPI-0



U1D FPGA



Input-Output

PA3GSB

AppMind

Sheet: /GPIO/

File: Raspberry-GPIO.sch

Title:

Size: A4

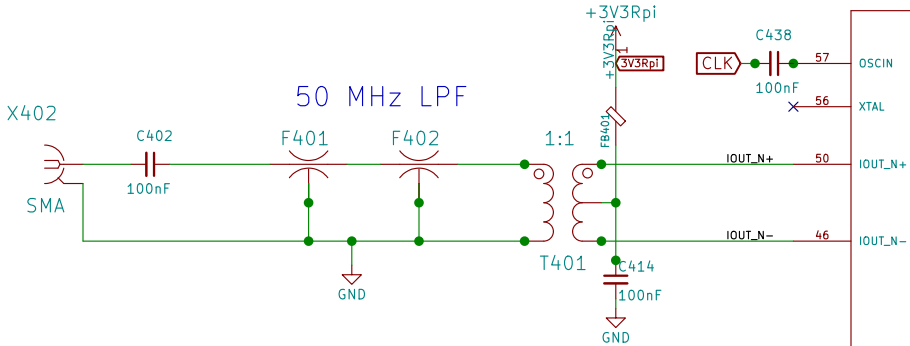
Date:

KiCad E.D.A. kicad 4.0.6

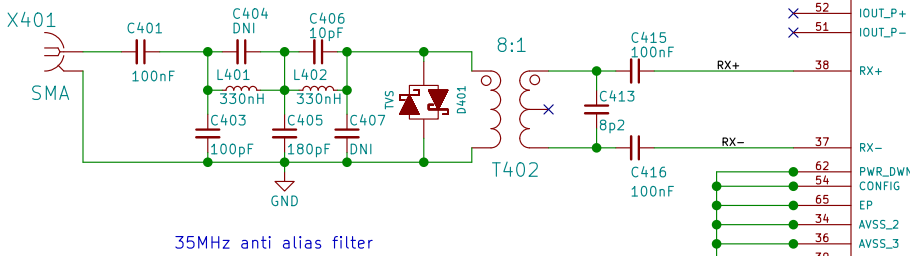
Rev: rev 2.0-beta1

Id: 3/4

RF-OUT

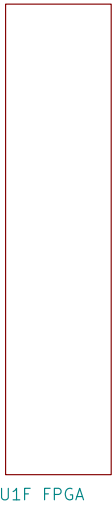
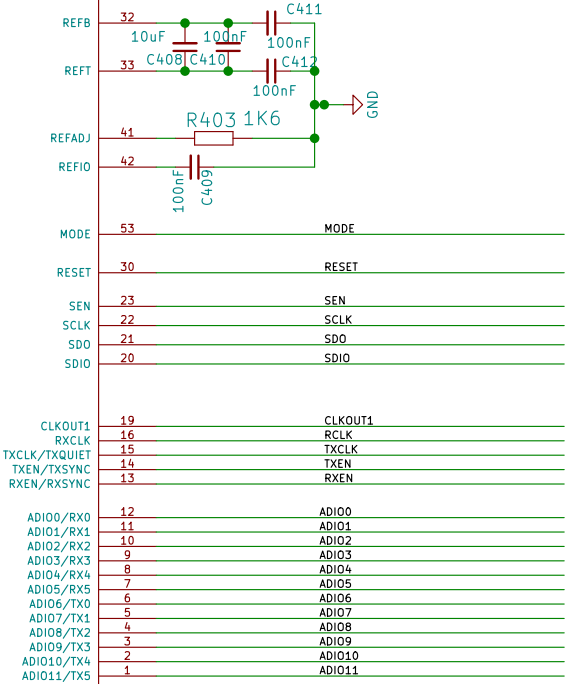


RF-IN



AD9866

IC401



PA3GSB

AppMind

Sheet: /Frontend/

File: Radioberry-Frontend.sch

Title:

Size: A3

Date:

Rev: rev 2.0-beta1

KiCad E.D.A. kicad 4.0.6

Id: 4/4